



1374.39812VV2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. YAMAZAKI, et al.  
Serial No.: 10/639,465  
Filed: AUGUST 13, 2003  
Title: FABRICATION METHOD FOR SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Group AU: 1763  
Examiner: George A. Goudreau  
Confirm. No.: 6745

**ADDITIONAL CLAIMS FEE TRANSMITTAL**

**Mail Stop: AMEND - FEE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 22, 2007

Sir:

Transmitted herewith is an **Amendment** in connection with the above-identified application.

**[X] Also attached: PTO-2038 (Credit Card Payment Form);**  
**Petition for Extension of Time**

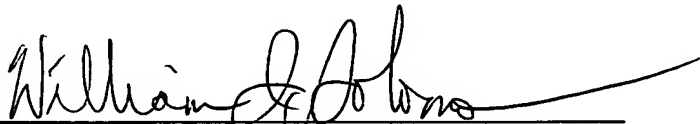
The fee (if applicable) has been calculated as shown below:

The fee (if applicable) has been calculated as follows:					
	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	31	- 29 =	2	x \$50 =	\$100.00
Independent Claims	3	- 3=	0	x \$200 =	\$00.00
Multiple Dependent Claim Fee	\$360.00				\$00.00
		TOTAL FEE DUE			<u>\$100.00</u>

- [X] A Credit Card Payment form (Form PTO-2038) in the amount of **\$220.00** is attached hereto.
- [X] The Commissioner is hereby authorized to charge any deficiencies of fees associated with this communication or credit any overpayment, to Deposit Account No. 01-2135, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17 (case 1374.39812VV2).

Respectfully Submitted,

**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

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